

GeSn-on-Si Avalanche Photodiodes with High Responsivity and Low Dark Current

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GeSn-on-Si avalanche photodiodes (APDs) are emerging as a promising solution for low-light detection in the short-wave infrared (SWIR) spectral range, including applications in imaging and telecommunications. In this work, key challenges such as high dark current and limited responsivity are addressed by demonstrating devices, which combine low noise with high signal amplification, while remaining compatible with silicon-based technology. GeSn-on-Si APDs with various Sn concentrations up to 1.9% are fabricated and characterized. The GeSn layers are grown pseudomorphically on Ge virtual substrates on Si wafers using molecular beam epitaxy. The devices comprise a double-mesa structure and exhibit a dark current dominated by a perimeter leakage path, independent of the Sn content. A dark current below 1 μA is maintained up to the onset of avalanche breakdown, marking a significant improvement compared to prior work. A record-high responsivity of 14.7 A W^{-1} is achieved at 1550 nm for the APD with 1.9% Sn. Through impulse response measurements, the 3-dB bandwidth is determined to 1.2 GHz on devices with an 80 μm diameter, resulting in a responsivity-bandwidth-product of $17.6 \text{ A W}^{-1} \text{ GHz}^{-1}$. These results highlight the potential of GeSn-on-Si APDs for high-performance, low-light applications in the SWIR range.

1. Introduction

Avalanche photodiodes (APDs) are widely used in applications requiring high responsivity, including optical communication, light detection and ranging (LiDAR), and imaging in the

short-wave infrared (SWIR) region.^[1–3] With applications spanning from quantum information systems to environmental monitoring, detection in the SWIR spectral range (1.1–3 μm) has become increasingly critical due to its unique advantages: it offers superior atmospheric transmission, reduced solar background, and eye-safe operation at wavelengths around 1550 nm.^[4] SWIR imaging has proven indispensable in fields like remote sensing, medical diagnostics, night vision, and chemical detection, all of which require detectors with high responsivity and low noise. APDs are particularly valuable in these areas due to their internal gain mechanism, which amplifies weak optical signals via carrier multiplication, enabling the detection of low-light and even single photons.^[5]

Today, APDs based on III-V compound semiconductors, especially those based on InGaAs with InP or InAlAs multiplication layers, dominate the SWIR market up to $\approx 1700 \text{ nm}$. These APDs offer excellent performance metrics,

with demonstrated data transmission rates up to 106 Gbit s^{-1} .^[6] For longer wavelengths in the extended SWIR and mid-infrared other material systems such as AlInAsSb, InAs or HgCdTe are used.^[7–9] However, these typically require cryogenic cooling to suppress dark current and noise, limiting their use in mobile or ambient-temperature environments. Moreover, III-V or II-VI compound semiconductors face noticeable limitations: they are expensive, and they rely on substrates that are smaller and incompatible with mainstream Si complementary metal-oxide-semiconductor (CMOS) technology.

To address these limitations, recent research has turned to group IV materials, such as Ge and GeSn. Ge-on-Si APDs were first demonstrated in 2007 and are now available in several Si photonics foundries, leveraging silicon's existing CMOS fabrication ecosystem.^[10,11] These materials offer excellent compatibility with CMOS fabrication processes, providing a path toward monolithic integration of SWIR photodetectors on Si: a critical advancement for scalable, low-cost SWIR technology. This progress was recently highlighted by the demonstration of CMOS-compatible 10-pixel linear arrays of Ge-on-Si APDs.^[12] GeSn alloys, formed by incorporating Sn into Ge, offer a tunable direct bandgap that extends the absorption edge of pure Ge from

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1.55 μm up to 4 μm at 15% Sn and beyond, depending on the Sn concentration.^[13] This tunability enables GeSn-on-Si APDs to operate at a broad range of wavelengths.

In SWIR APDs, the separate absorption, charge, and multiplication (SACM) architecture is commonly employed to leverage the advantageous properties of different materials. For GeSn-on-Si APDs, the absorption layer is composed of GeSn, while the multiplication layer is Si. Si, with its low ionization coefficient for holes relative to electrons, is an ideal material for achieving low-noise avalanche gain, outperforming III-V multiplication layers such as InP and InAlAs in this respect.^[14] Despite this promise, achieving low dark current in GeSn APDs remains a significant challenge, particularly at higher Sn concentrations where epitaxial defects and thermally generated carriers can degrade performance. GeSn-on-Si APDs have been under investigation since 2015, following the pioneering demonstration by Dong et al., who realized a SACM-structured device incorporating 5% Sn.^[15] Their results showed avalanche behavior and a responsivity of $\approx 1 \text{ A W}^{-1}$ at 1630 nm, validating the feasibility of GeSn as an absorber for extended SWIR detection. However, these devices were constrained by relatively high dark current larger than 100 μA before breakdown, moderate multiplication gains of approximately 10, and no demonstrated high-frequency performance. These limitations were largely attributed to the missing electric field management within the SACM structure and the early-stage of GeSn epitaxy at the time, which resulted in high threading dislocation densities. Subsequent studies predominantly explored GeSn p-i-n photodiodes, a structure consisting of a p-type, intrinsic, and n-type region, to improve material quality, while advancements in APD performance remained relatively modest.^[16] More recently, refined growth techniques and device architectures have begun to address these issues, but further improvements are necessary to approach the maturity of Ge-on-Si and III-V-based APDs.

In parallel, APDs are increasingly explored for single-photon detection applications. This capability has been demonstrated in both Ge-on-Si and InGaAs/InP APDs.^[17,18] Low dark count rates and high detection efficiency are essential for single-photon applications. While Ge-on-Si single-photon avalanche diodes (SPADs) have exhibited single-photon detection capability at room-temperature, this has so far only been demonstrated at a wavelength of 1310 nm.^[19] Similarly, InGaAs/InP SPADs are limited to around 1670 nm.^[20] GeSn, with its tunable bandgap, is therefore uniquely positioned to extend single-photon detection further to longer wavelengths within the SWIR range while remaining compatible with the Si technology. This could open the path to a wide range of quantum applications at longer wavelengths that are inaccessible with existing SPAD technologies.^[21]

In this work, GeSn-on-Si APDs are developed and investigated with the goal of combining high responsivity, low dark current, and compatibility with Si integration platforms. Building on previous insights into surface leakage and electric field distribution, a tailored fabrication process for double-mesa structures was used. The double-mesa design addresses the challenge of high peripheral electric field and the resulting high dark current, an issue we previously investigated in Ge-on-Si APDs and SPADs.^[22,23] The absorber layers are realized through high-quality molecular beam epitaxy (MBE) of pseudomorphic GeSn absorber layers on so-called “Ge-virtual substrates” (VS) on Si, allowing for efficient absorption of 1550 nm and beyond. Through

these improvements a record-high responsivity at 1550 nm of 14.7 A W^{-1} with a dark current below 1 μA at the onset of avalanche breakdown are achieved. Our results indicate a promising pathway for high-performance, Si-compatible APDs in SWIR applications and underscore the potential of GeSn-on-Si APDs in applications ranging from telecommunications to single-photon SWIR detection and pave the way for further advancements in CMOS-integrated optoelectronics.

2. Device Overview and Working Principle

The GeSn-on-Si APDs were realized via MBE followed by a standard clean-room fabrication procedure to form circular shaped mesa diodes (details in Experimental Section). The device structure is illustrated in **Figure 1**, which includes a schematic cross-section and a top-view scanning electron microscopy (SEM) image of the completed device.

The diameter of the device d_{diode} refers to the diameter of the second mesa. The first mesa is structured to be smaller by a distance of x_{etch} , which is 5 μm for the 80 μm diameter device, resulting in a 70 μm diameter of the first mesa. Electric field simulations revealed that x_{etch} can be reduced to 2.5 μm without influencing the peripheral electric field. The optical window has a diameter of 48 μm . **Figure 2a** presents a schematic band diagram of the GeSn-on-Si APDs under reverse bias, illustrating their working principle. Photons with an energy greater than the bandgap energy of GeSn are absorbed, generating electron-hole pairs. The photogenerated electrons drift into the Si multiplication layer, where they undergo impact ionization, leading to avalanche gain. The band diagram shown is an approximate representation based on simulations and does not precisely depict the band-offset energies at the GeSn heterojunction. The GeSn-on-Si APDs demonstrated here utilize a double-mesa structure, which effectively reduces the peripheral electric field at the mesa sidewalls, while maintaining a high electric field in the active area of the device.^[22,23] This field management suppresses surface leakage and enables low dark current without degrading responsivity and is confirmed by electric field simulations, as shown in **Figure 2b**. Simulation details can be found in the Experimental Section.

3. Results and Discussion

Following growth and fabrication, high-resolution x-ray diffraction (HR-XRD) measurements are carried out on all samples to determine the Sn content, the strain and overall crystal quality. **Figure 3a** shows the measurement of the symmetrical (004) diffraction as a high-resolution rocking curve (RC). The left graphs show the measurements of all three samples in the angle range between 65° and 67.5° , showing three distinct peaks. These are the GeSn peak of the absorption layer, the p^+ Ge peak arising from the extrinsically doped contact layer, and the Ge-VS peak, whereby only the GeSn reflection differs depending on the Sn content.^[24] On the right, the narrow and high Si(004) reflections of each sample are displayed, which arise from the Si substrate. Additionally, the lateral lattice constant is determined from a reciprocal space map measurement of the asymmetric $(\bar{2} \bar{2} 4)$ diffraction, and the corresponding space map is illustrated in **Figure 3b**.

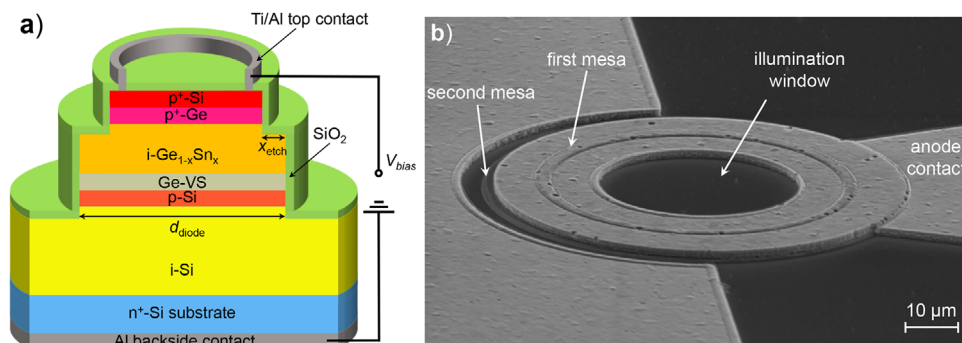


Figure 1. a) Schematic structure of the double-mesa GeSn-on-Si APDs and b) a top-view SEM image of a fabricated device with a diameter of 80 μm.

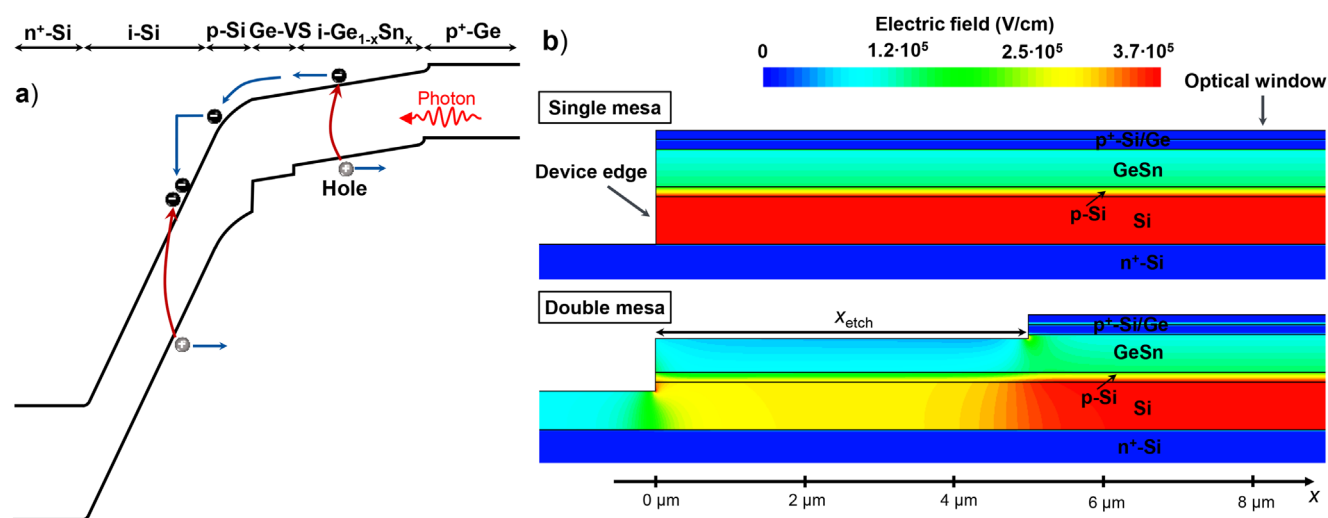


Figure 2. a) Schematic band diagram of the GeSn-on-Si APDs under reverse bias illustrating photon absorption and carrier multiplication. b) Simulated electric field distribution at the device edge, comparing the double-mesa structure to a conventional single mesa. The diameter of the device is 80 μm and the optical window and central active region extend toward the right-hand side, where the electric field remains high to allow for efficient charge transport and avalanche multiplication.

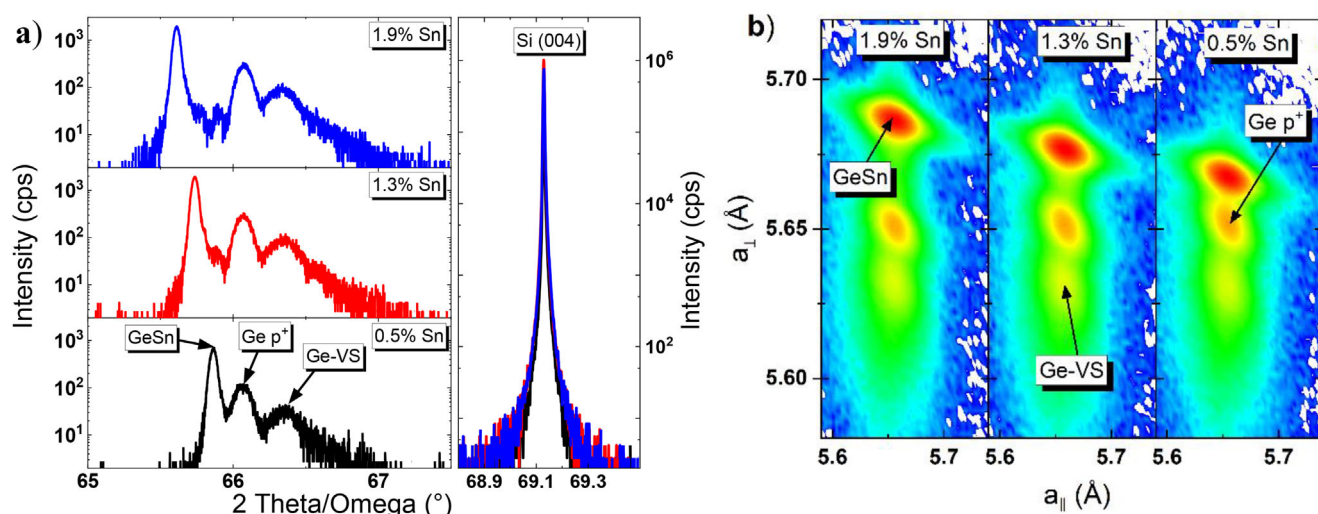


Figure 3. a) HR-XRD rocking curves of the symmetric (004) diffraction of the GeSn-on-Si APDs with various Sn concentration and b) the HR-XRD space maps of the asymmetric (224) diffraction of all samples. The GeSn absorption layer is pseudomorphically grown on the Ge-VS.

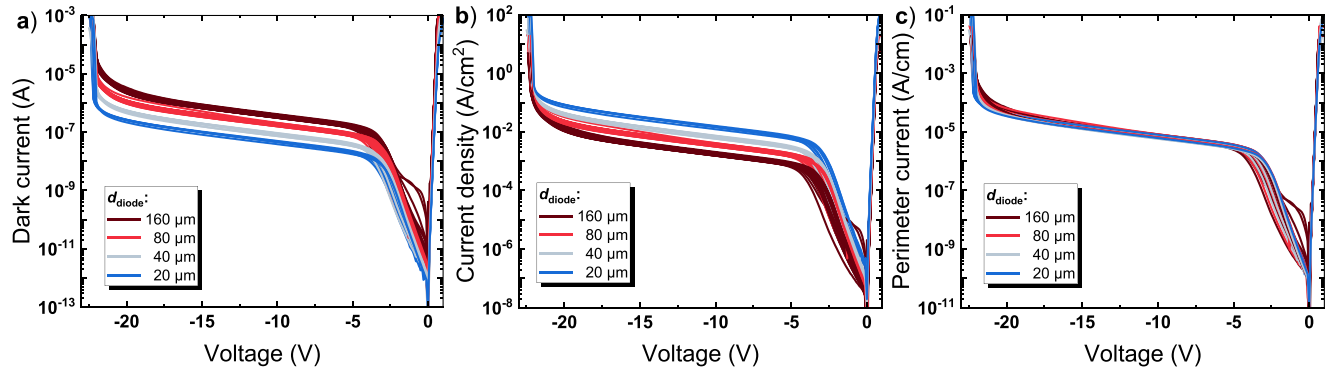


Figure 4. a) I_d of the double-mesa structure GeSn-on-Si APDs with 1.9% Sn for various diode diameters, b) I_d normalized to the area and c) I_d normalized to the perimeter of the devices indicating a leakage path on the perimeter of the diodes.

The space map shows the same three peaks. The p^+ Ge peak exhibits equal in-plane $a_{||}$ and out-of-plane lattice parameters $a_{\perp}$, indicating that the layer is fully relaxed. In contrast, the GeSn peak shows a lateral lattice constant matching that of the underlying Ge-VS, confirming that it is pseudomorphic. By extracting the respective in-plane and out-of-plane lattice parameters for each individual peak, the unstrained lattice constant a_0 can be calculated by applying the linear elasticity theory:

$$\frac{a_{\perp}}{a_{||}(\text{vs})} = 1 + f \left(1 + \frac{2c_{12}}{c_{11}} \right) \quad (1)$$

This assumes biaxial deformation with the elastic constants c_{11} and c_{12} and the lattice mismatch f .^[25] The reference grid for the pseudomorphic growth is the in-plane lattice spacing $a_{||}$ (VS) of the virtual substrate. The biaxial strain ϵ is the negative of f , e.g. larger lattice constant a_0 ($f > 0$) provokes a compressive strain ($\epsilon < 0$). With the help of Vegard's law, the Sn concentration can be determined for each sample.^[26] The Sn content in the GeSn-on-Si APDs are 0.5%, 1.3%, and 1.9% respectively. The GeSn layers are pseudomorphic with a compressive strain, which increases for higher Sn concentrations. In the 0.5% Sn sample the strain is -0.13% , in the 1.3% it is -0.17% , and in the 1.9% Sn sample it is -0.29% . While the Sn leads to a reduction in the bandgap energy

the compressive strain partly counteracts this and increases the bandgap energy compared to the unstrained state.

Figure 4a displays the dark current I_d of the APD with 1.9% Sn as a function of reverse bias voltage for devices of various diameters. The I_d increases with both reverse voltage and device diameter. Additionally, normalizing the I_d to the device area or perimeter reveals that the primary leakage current is perimeter-dependent, as observed in **Figure 4b,c**. This behavior contrasts typical GeSn-photodiodes, where I_d scales with device area due to bulk-dominated leakage.^[27] The observed perimeter dependence indicates suppressed bulk leakage, made possible by high crystal quality and low defect density, pointing to the effectiveness of the epitaxial growth approach. This was observed in all samples regardless of the Sn concentration, as can be seen in the **Figures S1 and S2**, Supporting Information.

Figure 5a illustrates the dark current–voltage characteristics of the GeSn-on-Si APDs with varying Sn concentrations (0.5%, 1.3%, and 1.9%) for a fixed device diameter of $d_{\text{diode}} = 80 \mu\text{m}$. The results indicate no clear influence of Sn concentration on the I_d . Also, there is no influence of the Sn concentration on the breakdown voltage V_{BD} because V_{BD} is predominantly defined by the Si multiplication layer thickness. Nevertheless, V_{BD} varies slightly between -21.6 V for the 0.5% Sn and 1.3% Sn and -22.1 V for the 1.9% Sn samples, which is caused by minor thickness fluctuations of the Si multiplication layer. Temperature-dependent

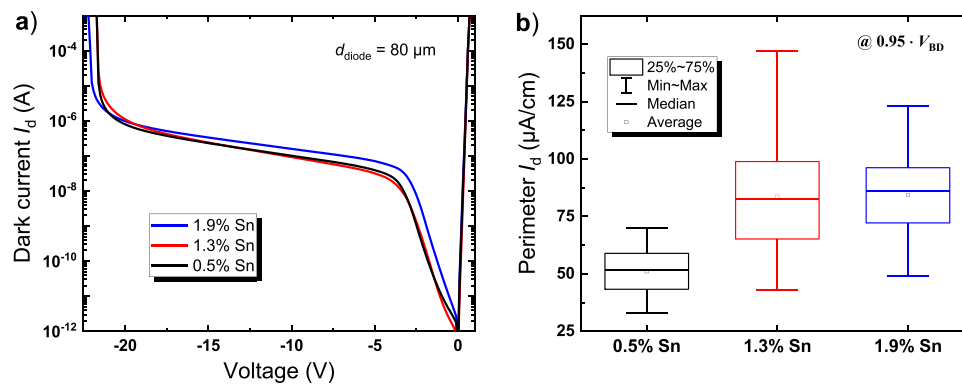


Figure 5. a) Comparison of the I_d -characteristics for different Sn concentrations and b) the box-plot of the perimeter I_d at $0.95 V_{\text{BD}}$. The box plot consists of data of approximately 70 diodes for each sample.

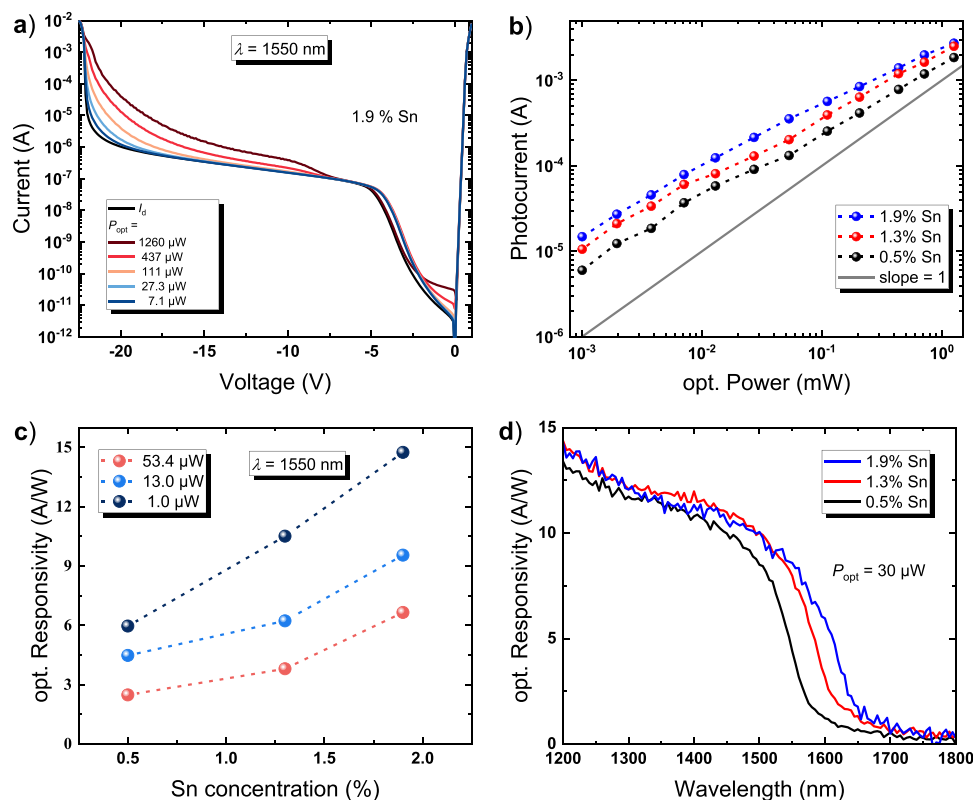


Figure 6. a) The current of the Ge-on-Si APD with 1.9% Sn under illumination of 1550 nm for P_{opt} ranging from 7.1 μW to 1.26 mW, and b) the calculated photocurrent as a function of the Sn concentration for various P_{opt} , and c) R_{opt} for three P_{opt} as a function of Sn concentration, and d) spectral R_{opt} measured at 30 μW using a ps-pulsed laser. Measurements were performed at a bias of -21.6 V for the 0.5% Sn and 1.3% Sn samples, and at -22.1 V for the 1.9% Sn sample.

analysis of the V_{BD} revealed a positive temperature coefficient of ≈ 0.013 V K^{-1} , confirming that the breakdown mechanism is indeed avalanche multiplication, see Figure S3, Supporting Information.

The devices were statistically analyzed for their I_{d} -characteristics using approximately 70 devices with four different device diameters ($d_{\text{diode}} = 160, 80, 40,$ and 20 μm) across each sample. Figure 5b illustrates the box-plot of the perimeter current at a bias of 95% of V_{BD} , including the minimum, maximum, median, and mean values, along with the interquartile range (25%–75%) for each Sn concentration. The analysis shows that I_{d} is uniform across each sample, with the mean perimeter current increasing slightly from 50 to 87.5 $\mu\text{A cm}^{-1}$ with higher Sn concentrations. This trend may result from the reduced bandgap of the GeSn alloy with increasing Sn content. Overall, the I_{d} in this work remains significantly lower than previously reported values. For example, the I_{d} at 95% of V_{BD} were reported as 85.9, 95.1, 2.73, and 30 mA cm^{-1} in earlier GeSn-on-Si APDs.^[15,28–30] In comparison, the APDs with 1.9% Sn presented here achieve an average I_{d} of 84.37 $\mu\text{A cm}^{-1}$, which is a five-fold reduction relative to the previously reported lowest value of 450.8 $\mu\text{A cm}^{-1}$.^[31] Figure S4, Supporting Information, illustrates the I_{d} -characteristics of this work in direct comparison with all previously reported GeSn-on-Si APDs. The significantly lower I_{d} achieved here suggests a pathway toward reduced dark count rates under Geiger-mode operation, which could make

GeSn-on-Si APDs promising candidates for single-photon detection at 1550 nm. However, a systematic study of SPAD operation characteristics will be required to confirm this potential and will be the subject of future investigations.

Following the I_{d} analysis, measurements under illumination were carried out. Figure 6a depicts the current-voltage behavior of the GeSn-on-Si APD with 1.9% Sn under various incident optical power levels ranging from 7.1 to 1260 μW . A rise in current under illumination can be observed at around -8 V, which corresponds to the punch-through of the electric field into the GeSn absorption layer. At this voltage, the depletion region expands beyond the charge layer and begins to extend into the GeSn layer, enabling efficient separation and collection of photogenerated carriers. This transition also coincides with a local minimum in the device capacitance, as shown in of Figure S5, Supporting Information, reflecting the full depletion of the charge layer. However, the photocurrent at this voltage remains smaller than expected from a reference GeSn p-i-n diode, which is used to benchmark absorption efficiency. This discrepancy can be attributed to enhanced recombination in the GeSn layer or to residual energy barriers at the GeSn/Ge heterointerface that limit efficient carrier injection into the Si multiplication region. As the reverse bias increases further, these limitations are overcome, and the current rises strongly due to carrier multiplication, with unity gain achieved only at -20.8 V. The gain as a function of reverse voltage is provided in Figure S6, Supporting Information. The

maximum photocurrent is generally observed at or slightly below the V_{BD} . This behavior is consistent across all three samples, as the overall shape of the photocurrent curve remains similar, although the absolute photocurrent decreases with lower Sn concentration. These measurements were performed identically for all samples, and the corresponding graphs are provided in Figure S7, Supporting Information.

Photocurrent measurements were also conducted for several more laser powers on all three samples. The results of these measurements are shown in Figure 6b–d, taken at a bias of -21.6 V for the 0.5% Sn and 1.3% Sn samples, and at -22.1 V for the 1.9% Sn sample. Figure 6b presents the measured photocurrent as a function of incident laser power. In the double logarithmic plot, the photocurrent follows a straight line with a slope different from one, indicating a nonlinear power-law relationship. This deviation arises from space-charge effects. At higher optical powers, the density of photogenerated carriers becomes large enough that their accumulated charge partially screens the internal electric field in the multiplication region. The reduced field not only slows carrier transport from the GeSn absorber into the Si multiplication layer but also lowers the impact ionization rate. As a result, the multiplication gain decreases with increasing optical power. This type of intensity-dependent responsivity has also been reported for Ge-on-Si and InGaAs APDs.^[32,33] Figure 6c illustrates the optical responsivity R_{opt} , calculated by dividing the photocurrent by the laser power P_{opt} , as a function of the Sn concentration for three laser powers. A strong increase of the R_{opt} with increasing Sn concentration can be observed. At a laser power of $53.4 \mu\text{W}$, the devices achieve R_{opt} values of 6.65, 3.80, and 2.48 A W^{-1} for Sn concentrations of 1.9%, 1.3% and 0.5%, respectively. The maximum R_{opt} are obtained at the lowest optical power of $1 \mu\text{W}$, reaching 14.7 A W^{-1} (1.9% Sn), 10.5 A W^{-1} (1.3% Sn), and 6.0 A W^{-1} (0.5% Sn). These results represent a significant improvement over other recent works.^[30] By dividing by the R_{opt} of a reference GeSn p-i-n diode ($R_{opt,PD} = 0.095 \text{ A W}^{-1}$) with the same layer thickness, the multiplication gain can be calculated. This results in a gain of 154 for the 1.9% Sn sample.

Additionally, the spectral response of the GeSn-on-Si APDs was investigated and the results are illustrated in Figure 6d. The high responsivity at 1550 nm demonstrates the suitability of these APDs for fiber-optic communication systems. Moreover, an extended sensitivity at wavelengths beyond 1550 nm is observed with increasing Sn concentration. This performance improvement is attributed to the reduced bandgap of GeSn, which allows for efficient absorption of longer-wavelength photons. This redshift in absorption and R_{opt} for wavelengths above 1700 nm can be seen clearer in the logarithmic scaled graph in Figure S8, Supporting Information. Future work could explore higher Sn concentrations to extend the device's ability to the emerging $2 \mu\text{m}$ telecom band. As our epitaxial approach with the double-mesa device structure leads only to a small influence of the Sn content, these GeSn-on-Si APDs with higher Sn concentration could exhibit similar noise characteristics, underscoring their potential as high-performance detectors in emerging telecom applications. There is a slight discrepancy between the responsivities achieved using the ps-pulsed laser and the cw-laser. We attribute this to the high peak carrier density generated by the ps-pulses, which can lead to gain saturation due to the space-charge effects. Additionally, Auger recombination may contribute to carrier loss under

these high-density conditions, further reducing the multiplication gain.

Lastly the bandwidth of the devices was characterized through impulse response measurement using the ps-pulsed laser and a sampling oscilloscope with a bandwidth of 16 GHz. Figure 7a displays the impulse response measurement of the GeSn-on-Si APD with 1.9% Sn at various bias voltages. As the reverse voltage increases, the amplitude of the response rises due to enhanced avalanche multiplication. Figure 7b presents the corresponding normalized frequency response extracted from the temporal signal. At reverse voltages below the V_{BD} of -22.1 V, a 3 dB bandwidth of 300 MHz is achieved for a device with $d_{diode} = 80 \mu\text{m}$. This relatively low value is caused by the large parasitic capacitance of approximately 8 pF, originating from the large metal pads. In contrast, the theoretical junction capacitance of the APD is estimated to be only ≈ 0.6 pF, which corresponds to a 3 dB bandwidth of 5.3 GHz when limited by the resistive-capacitive (RC) time constant. Optimization, such as reducing contact pad size or employing a localized buried n^+ layer, would therefore significantly increase bandwidth. Further improvement toward bandwidths ≈ 60 GHz is possible through device down-scaling to $\approx 15 \mu\text{m}$ diameter. Interestingly, for reverse voltages exceeding the $|V_{BD}|$, an increase in bandwidth can be observed while maintaining consistent signal amplitude. This behavior is caused by resonance due to inductive peaking, a phenomenon well-documented in other APDs.^[32] We recently reported that this also occurs in GeSn-on-Si APDs.^[34] Therefore a 3-dB-bandwidth of 1.2 GHz is achieved at a voltage of -22.9 V. Further exploitation of the demonstrated inductive peaking can be achieved if the impedance of the APD is carefully designed, as has been demonstrated by Shi et al. achieving a 53 GHz bandwidth in Ge-on-Si APDs.^[14]

In the literature a common figure of merit for comparing APDs is the gain-bandwidth-product (GBP), which evaluates their suitability for telecommunications applications. While the GBP successfully captures the trade-off between high gain and high bandwidths, it is only valid when comparing APDs made from the same material system. Because the gain is independent of absorption, the GBP does not fully account for the ability of the APD to detect specific optical signals. Therefore, we propose using the responsivity-bandwidth-product (RBP), which incorporates the signal strength by accounting for gain and quantum efficiency. Although less commonly used in the literature, the RBP offers greater insight into detection performance and allows for comparison across material systems.^[35] For the demonstrated GeSn-on-Si APD, the achieved RBP is $17.6 \text{ A W}^{-1} \text{ GHz}^{-1}$ at 1550 nm, which corresponds to a GBP of 185 GHz when considering the reference responsivity of 0.095 A W^{-1} .

A comparative analysis of the R_{opt} at 1550 nm versus the corresponding I_d at that bias is presented in Figure 8 for the GeSn-on-Si APDs from this study, alongside four previously reported devices, to highlight the advancements achieved in this work.^[15,28,29,36] The APDs fabricated in this work exhibit superior performance, achieving an R_{opt} of 14.7 A W^{-1} , which significantly exceeds the previously reported maximum of 4.88 A W^{-1} at a wavelength of 1550 nm. In terms of I_d , our APDs exhibit notably lower values under most operating conditions compared to earlier studies. However, at bias voltages approaching the V_{BD} , where the gain and R_{opt} reach their maximum values, the I_d

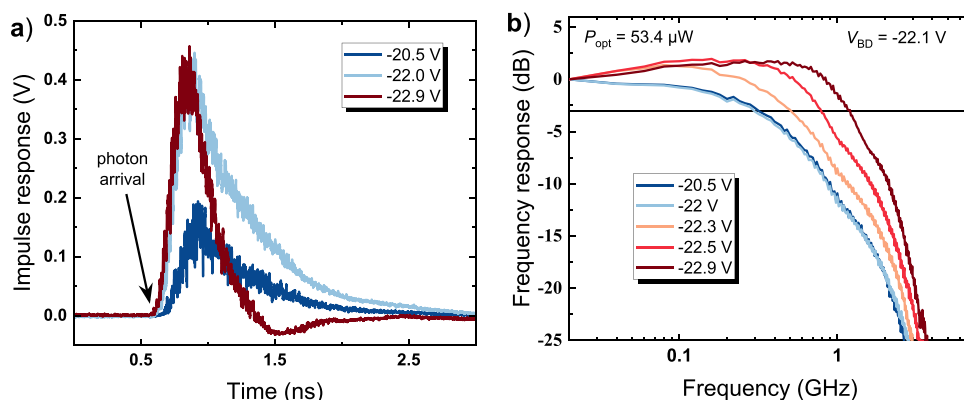


Figure 7. a) Impulse response at different bias voltages while illuminated by 53.4 μW at 1550 nm and b) normalized frequency response of an 80 μm diameter GeSn-on-Si APD with 1.9% Sn, showing an increased bandwidth at higher reverse voltages.

increases substantially. As a result, the I_d values under these conditions become comparable with those reported in other works from the literature. For consistent comparison, all dark current values are normalized to the device area, following common practice in the literature. While the dark current in our devices exhibits a clear perimeter dependence, we adopt area normalization to ensure compatibility with previously published values. This approach allows for direct benchmarking against prior work, even though it may underestimate the relative improvement achieved through our double-mesa design. Device diameters reported in the literature are 30 μm for ref.[15], 60 μm for refs.[28,29], and 160 μm for ref.[36]. The here presented performance improvements highlight the effectiveness of our fabrication process and material optimization strategies, such as the high-quality epitaxy on the Ge-VS, employed in this study. This demonstrates the potential of GeSn-on-Si APDs for high-sensitivity applications in the short-wave infrared spectral range.

In addition to R_{opt} and I_d , Table 1 also highlights bandwidth-related performance. Our devices achieve a 3 dB bandwidth

of 1.2 GHz with an 80 μm diameter, corresponding to a responsivity-bandwidth product of 17.6 $A W^{-1} GHz^{-1}$. While this is below the values reported for the most advanced Ge-on-Si and InGaAs/InP APDs, it represents a new benchmark for GeSn-on-Si devices. As discussed previously, bandwidths in the tens of GHz range should be achievable in GeSn-on-Si APDs through targeted device scaling and layout optimization.

Additionally, the highest-performing Ge-on-Si APDs reported in the literature were included to contextualize our results with respect to state-of-the-art APDs.[37–40] The data show that Ge-on-Si APDs achieve up to twice the R_{opt} while maintaining similar dark current densities compared to our GeSn-on-Si APDs. This performance advantage is primarily due to their use of significantly thicker absorption layers (typically $>1 \mu m$) and the overall maturity of the Ge material platform compared to GeSn. Particularly in the longer wavelength range above 1550 nm, where conventional Ge-on-Si APDs exhibit limited absorption our GeSn-on-Si APDs already demonstrate superior performance. These results highlight the strong potential of GeSn for extended SWIR detection and suggest that further improvements in epitaxy and device design could enable GeSn-on-Si APDs to surpass pure Ge devices in both responsivity and spectral range, making them highly promising for next-generation SWIR applications.

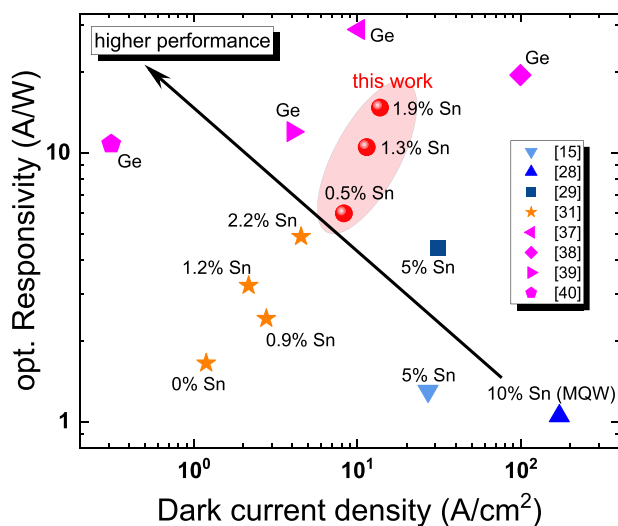


Figure 8. Comparison of the R_{opt} at 1550 nm versus the I_d for various GeSn-on-Si and Ge-on-Si APDs reported in the literature.

4. Conclusion and Outlook

In summary, we demonstrated high performance GeSn-on-Si APDs with Sn concentrations up to 1.9%. We attribute the improved performance of our devices compared to prior works to high-quality MBE growth of pseudomorphic GeSn absorber layers on Ge-VS on Si substrates combined with a tailored fabrication process for double-mesa structures. Our devices show a notably reduced dark current, and the APD with 1.9% Sn achieved a maximum responsivity of 14.7 $A W^{-1}$ at 1550 nm which is three times larger than previously reported values. Spectral responsivity measurements revealed an enhanced absorption at wavelengths beyond 1550 nm with increasing Sn concentrations. Additionally, impulse response measurements demonstrated a 3-dB bandwidth of 1.2 GHz for devices with an 80 μm diameter resulting in a responsivity-bandwidth-product of 17.6 $A W^{-1} GHz^{-1}$.

Table 1. Comparison of GeSn-on-Si APDs with Ge-on-Si and InGaAs/InP APDs operating at 1550 nm and room-temperature.

Material	Diameter [μm]	Breakdown voltage [V]	Responsivity [A W^{-1}]		Dark current [A]		3 dB Bandwidth [GHz]	Responsivity-bandwidth-product [$\text{A W}^{-1} \text{GHz}$]
			Unity gain	Maximum	Unity gain	Maximum		
This work	80	21.1	0.095	14.7	$1.5 \cdot 10^{-6}$	$43 \cdot 10^{-6}$	1.2	17.6
GeSn-on-Si ^[29]	60	14.5	0.15	4.4	$5.3 \cdot 10^{-6}$	$884 \cdot 10^{-6}$	1.0	4.4
GeSn-on-Si ^[15]	30	9.8	0.16	1.3	$1.6 \cdot 10^{-5}$	$9.8 \cdot 10^{-4}$	—	—
GeSn-on-Si ^[31]	80	21.7	0.11	0.4	$1.9 \cdot 10^{-6}$	$1.7 \cdot 10^{-5}$	—	—
Ge-on-Si ^[38]	10	26.5	0.35	19.5	$20 \cdot 10^{-9}$	$78 \cdot 10^{-6}$	12	234
Ge-on-Si ^[37]	50	55	0.41	28.8	$200 \cdot 10^{-9}$	$200 \cdot 10^{-6}$	—	—
InGaAs/InP ^[41]	35	33	0.94	36.7	$0.14 \cdot 10^{-9}$	$28 \cdot 10^{-9}$	8.3	304

While these results establish a new performance benchmark for GeSn-on-Si APDs, several opportunities for further advancement remain. The current bandwidth is limited by parasitic capacitance from the large contact pads. By optimizing device geometry and contact layout, the intrinsic junction capacitance could enable multi-GHz bandwidths, and device miniaturization down to $\approx 15 \mu\text{m}$ diameter or waveguide-coupled designs could push operation into the tens of GHz regime.^[14] For imaging applications, scaling the circular mesa devices into pixel arrays is feasible, though careful optimization of the double-mesa geometry will be required to preserve fill factor. Beyond linear operation, the demonstrated devices are also promising candidates for single-photon detection. With proper operation in Geiger mode, the achieved low dark current could enable GeSn-on-Si SPADs that extend single-photon sensitivity beyond the wavelength range accessible with existing Ge-on-Si and InGaAs/InP devices.^[42] Finally, increasing Sn concentration in the absorber provides a clear pathway to extend detection into the $2 \mu\text{m}$ telecom band and beyond.^[43] While our current approach using pseudomorphic growth offers superior crystal quality, it is constrained by a critical thickness that limits absorber thickness at high Sn contents.^[44] Strain-relaxed GeSn buffer layers, already employed in high-performance GeSn lasers, represent a promising alternative for realizing thicker absorbers at 10–12% Sn.^[45] Overcoming the associated defect-related leakage will be key to enabling GeSn-on-Si APDs for mid-infrared detection and integrated optoelectronic platforms.

5. Experimental Section

Molecular Beam Epitaxy: The epitaxial sample structure was grown using molecular beam epitaxy (MBE).^[46] The growth process started on n^+ -Si (100) substrates with a doping concentration of $2 \cdot 10^{19} \text{ cm}^{-3}$, which act as the cathode contact. Before the MBE, the Si substrates underwent a thorough cleaning protocol involving sequential treatments with piranha solution, RCA 1 and 2, and HF solutions to ensure a contamination-free surface. The first layer deposited was a 500 nm thick (intrinsic) i-Si, serving as the multiplication layer. Hall measurements on test structures confirmed that the background doping is n-type with a concentration of approximately $1 \cdot 10^{14} \text{ cm}^{-3}$. Following this, a 100 nm thick charge layer was added, featuring a nominal p-type doping of $1.4 \cdot 10^{17} \text{ cm}^{-3}$ to sustain a low electric field within the GeSn while ensuring effective impact ionization in the i-Si layer. To grow low-defect GeSn absorber layers, a lattice-matched Ge-virtual substrate (Ge-VS) on Si using a two-step epitaxial growth process was first created. A Ge-VS is a relaxed Ge buffer layer deposited on a Si wafer to accommodate the lattice mismatch between Si

and Ge-based materials. This intermediate layer reduces strain and suppresses defect propagation into subsequent epitaxial layers. This process involves the low-temperature growth of a 100 nm thick Ge seed layer, followed by high-temperature annealing at approximately 820°C . In previous studies, it is demonstrated that this approach results in low-dark current Ge p-i-n diodes and yields threading dislocation densities on the order of $1 \cdot 10^7 \text{ cm}^{-2}$.^[47] Subsequently, a 300 nm thick intrinsic GeSn absorption layer was grown on the Ge-VS at a substrate temperature of 160°C . Using this approach, the GeSn grows pseudomorphically without generating additional threading dislocations. Both the Ge and GeSn have a p-type background doping with a doping concentration of $5 \cdot 10^{15} \text{ cm}^{-3}$ in the Ge and $1 \cdot 10^{16} \text{ cm}^{-3}$ in the GeSn.^[48] The Sn concentration of the GeSn layer was varied in three different samples to later analyze the influence of the Sn on device performance. Both the i-Si and the GeSn were specifically designed to be thin to achieve a low breakdown voltage, easing integration goals. The anode contacts of the GeSn-on-Si APDs consist of a 211 nm thick Ge/Si p+ hetero layer, doped at $1 \cdot 10^{20} \text{ cm}^{-3}$. This structure was chosen to provide effective surface passivation and ease of fabrication.

X-Ray Diffraction: High-resolution X-ray diffraction (HR-XRD) was performed on all fabricated samples to assess the structural properties of the epitaxial layers. A Rigaku Smartlab system equipped with a Cu $K\alpha 1$ source ($\lambda = 1.5406 \text{ \AA}$) was used. The symmetrical (004) diffraction was measured using a 0D detector, and the asymmetric ($\bar{2} \ 2 \ 4$) diffraction using a 1D detector.

Device Fabrication: The fabrication of the devices followed standard cleanroom procedures, starting with mesa formation through inductively coupled plasma reactive ion etching (ICP-RIE) using HBr as the etching gas. The process used 600 W coil power with 120 W platen power at a chamber pressure of 2.5 mTorr. The first mesa has an etch depth of approximately 250 nm, as it is necessary to etch through the Ge/Si p+ layer to ensure proper device functionality. The second mesa was etched an additional 500 nm to safely stop inside the Si multiplication layer. The samples were cleaned in ultrasonic baths of acetone and isopropanol, followed by a rinse in deionized water. Native oxide layers were removed using a sequential dip in 2.5% HF and 13% HCl solutions. The SiO_2 passivation layer was deposited using a plasma-enhanced chemical vapor deposition using tetraethyl orthosilicate and O_2 as the precursors at a temperature of 390°C . The process parameters were 20 W of plasma power at a chamber pressure of 265 mTorr. The contact pads were structured through reactive ion etching with CHF_3 gas at a chamber pressure of 38 mTorr and 150 W plasma power. The metal contacts were deposited using Ar-sputtering and consist of a Ti/Al-stack (50 nm/1200 nm). The Ti interlayer prevents Al spiking. The metal contacts were again structured using ICP-RIE with HBr.

Simulation: Electric field simulations and schematic energy band diagrams were generated using the Sentaurus TCAD suite from Synopsys. The simulated layer structure corresponds exactly to the experimental device stack described in the epitaxy section, including the GeSn absorption layer, intrinsic Ge spacer, charge layer, and Si multiplication region. A fine mesh of 10 nm by 10 nm was implemented. The simulations were used to investigate the vertical electric field distribution across the APD. The simulations guided the development of the

double-mesa design by helping to assess lateral field spreading near the mesa edge.

Electro-Optical Characterization: The electro-optical characterization of all samples is carried out using an on-wafer measurement system at a controlled temperature of 293 K. The devices are contacted using probe needles and measured through a Keithley 2636B source-measure unit. By employing low-leakage triaxial cables, the overall noise floor of the measurement setup was reduced to below 1 pA. For the illumination a continuous-wave (cw) laser at 1550 nm was vertically coupled onto the devices via a single-mode fiber. The laser power was calibrated using an In-GaAs photodiode. Overall the current–voltage measurements show great accuracy and reproducibility. Even at high reverse voltage, where the dark current is elevated and the maximum R_{opt} values are achieved, the current measurement accuracy is ≈ 40 nA, while the minimum measured photocurrent is in the range of 14 μA . Additionally, the spectral response of the GeSn-on-Si APDs was investigated using a picosecond (ps)-pulsed supercontinuum white-light laser (NKT SuperK Extreme EXR-15) equipped with an acousto-optic tunable filter. The measurements were performed between 1200 nm and 1800 nm in wavelength steps of 5 nm. Prior to the measurements the laser power was calibrated to deliver a constant power of 30 μW for all wavelengths as otherwise the gain of the APD would be wavelength dependent. The impulse response measurements were performed using the same ps-pulsed laser at 1550 nm. The devices were mounted on a custom high-frequency printed-circuit board out of Poly-tetrafluoroethylene and connected to a SMA connector. The devices were then connected to a direct-current (dc) source and a sampling oscilloscope (PicoScope 9404-16) with a bandwidth of 16 GHz through a bias tee (Anritsu K251).

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

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Conflict of Interest

The authors declare no conflict of interest.

Data Availability Statement

The data that support the findings of this study are openly available at Zenodo, reference <https://doi.org/10.5281/zenodo.15746540>.

Keywords

avalanche photodiodes, dark current, GeSn, molecular beam epitaxy, responsivity, short-wave infrared, telecommunications

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